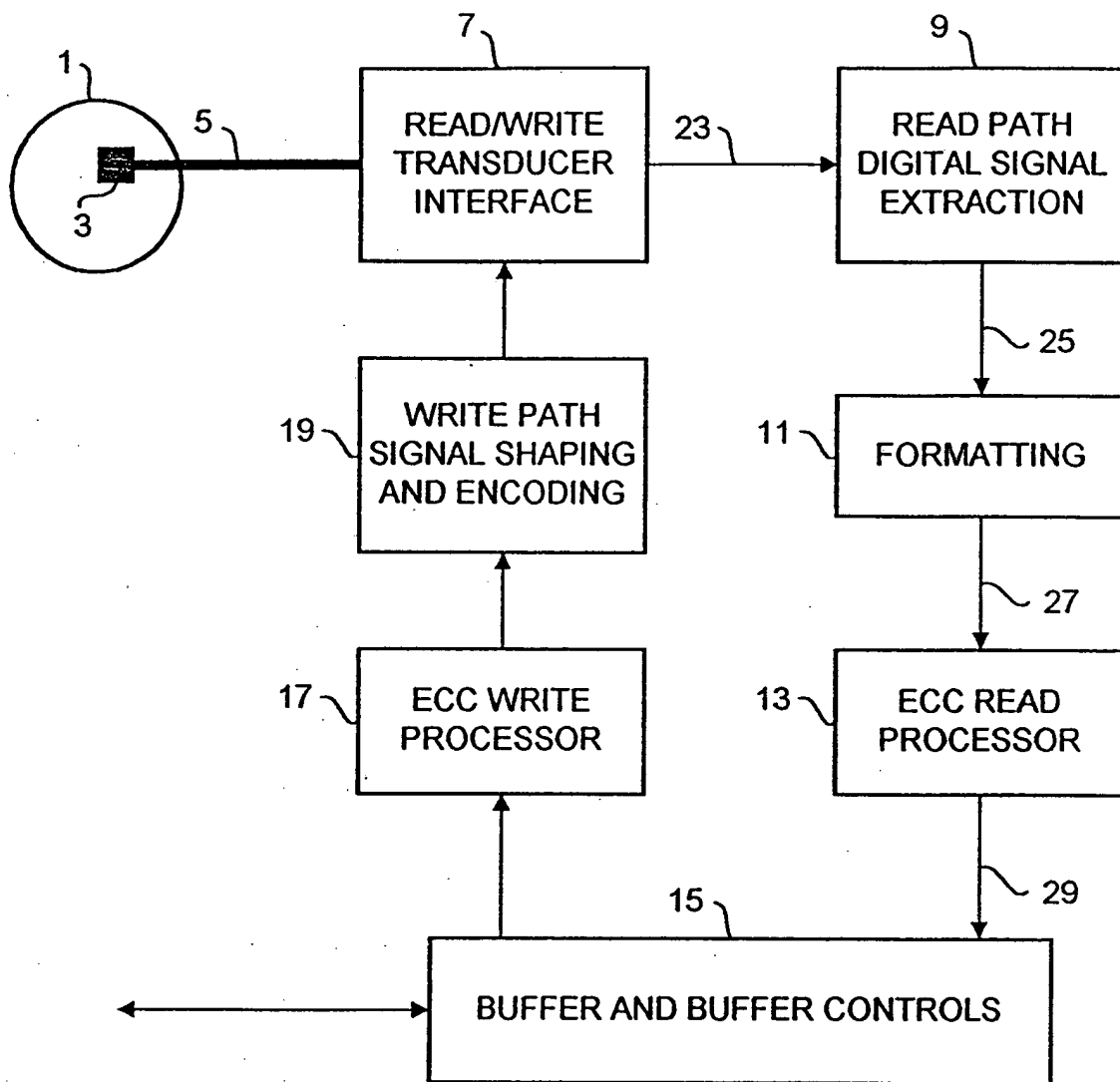


2004-05-04



PARTIAL READ AND WRITE PATH DATA FLOW
IN A DIRECT ACCESS STORAGE DEVICE

FIG. 1
(Prior Art)

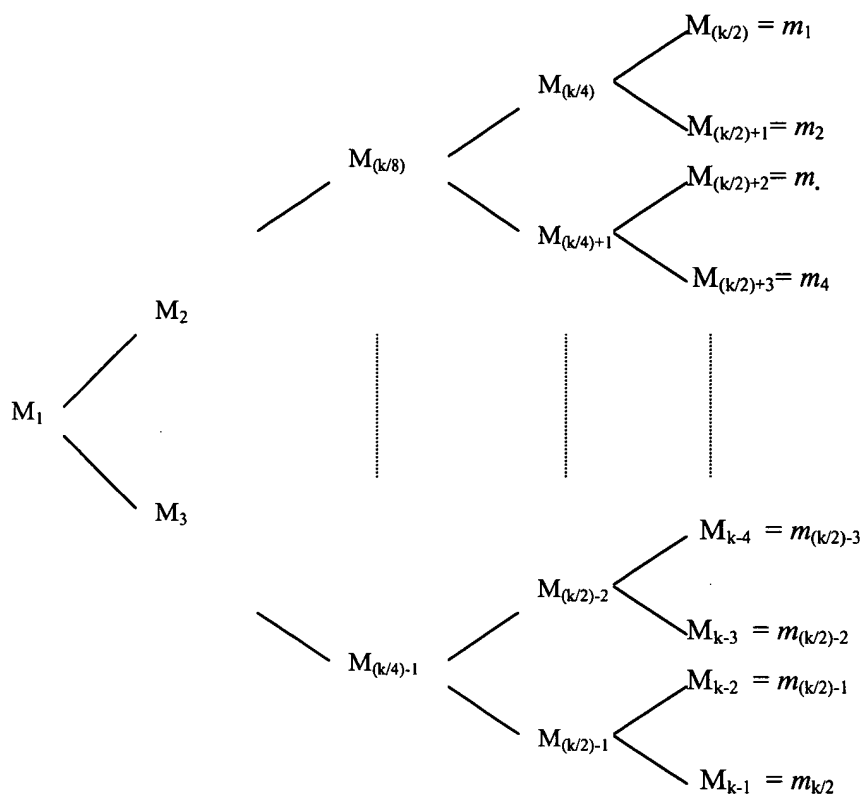


FIG. 2

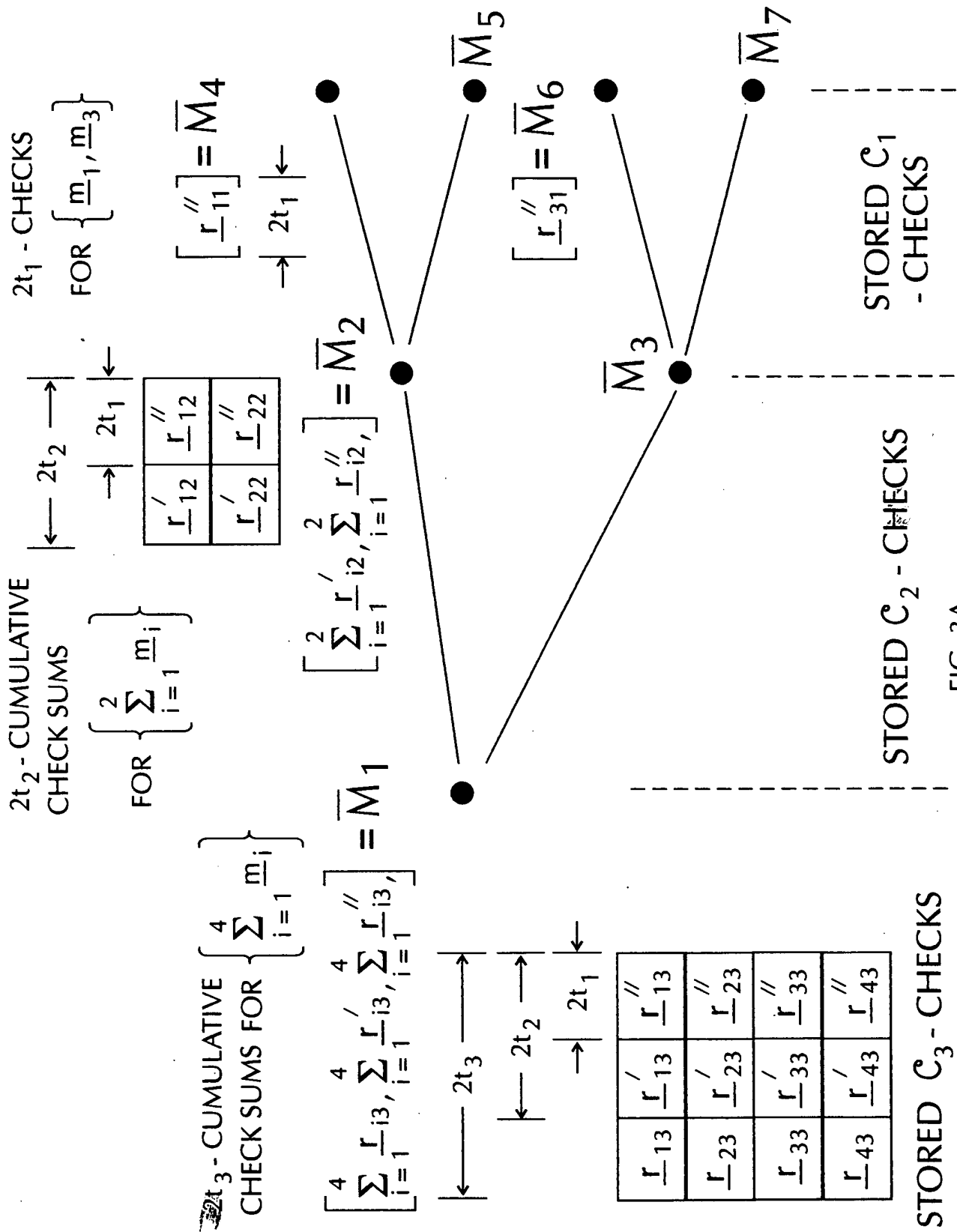


FIG. 3A

MULTI-LEVEL ENCODER

CHECK EQUATIONS

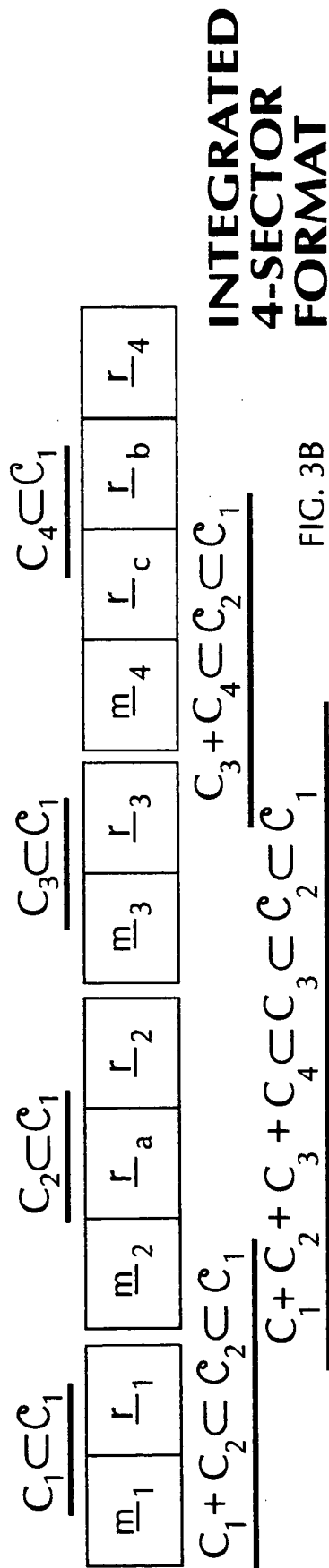
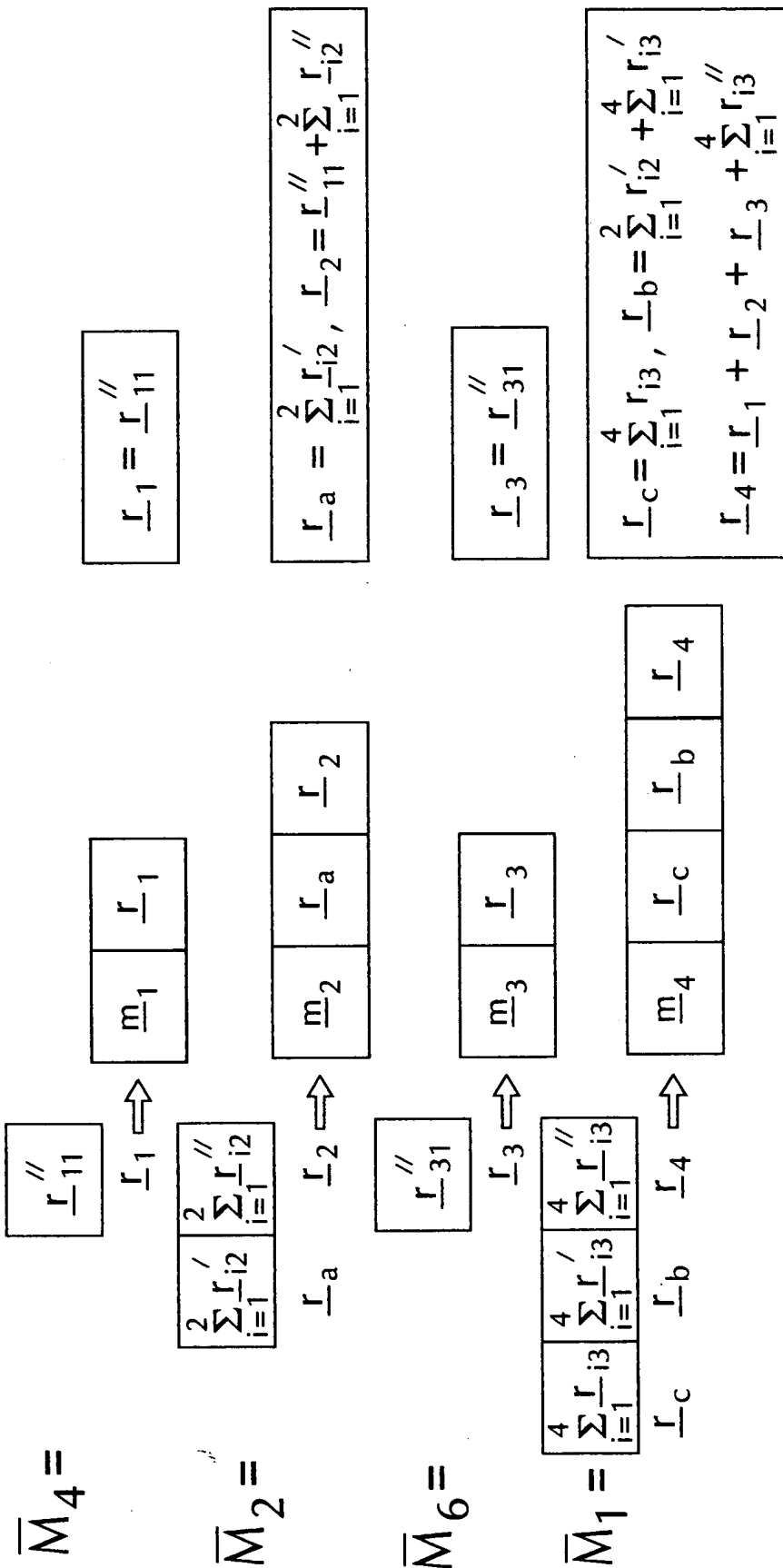
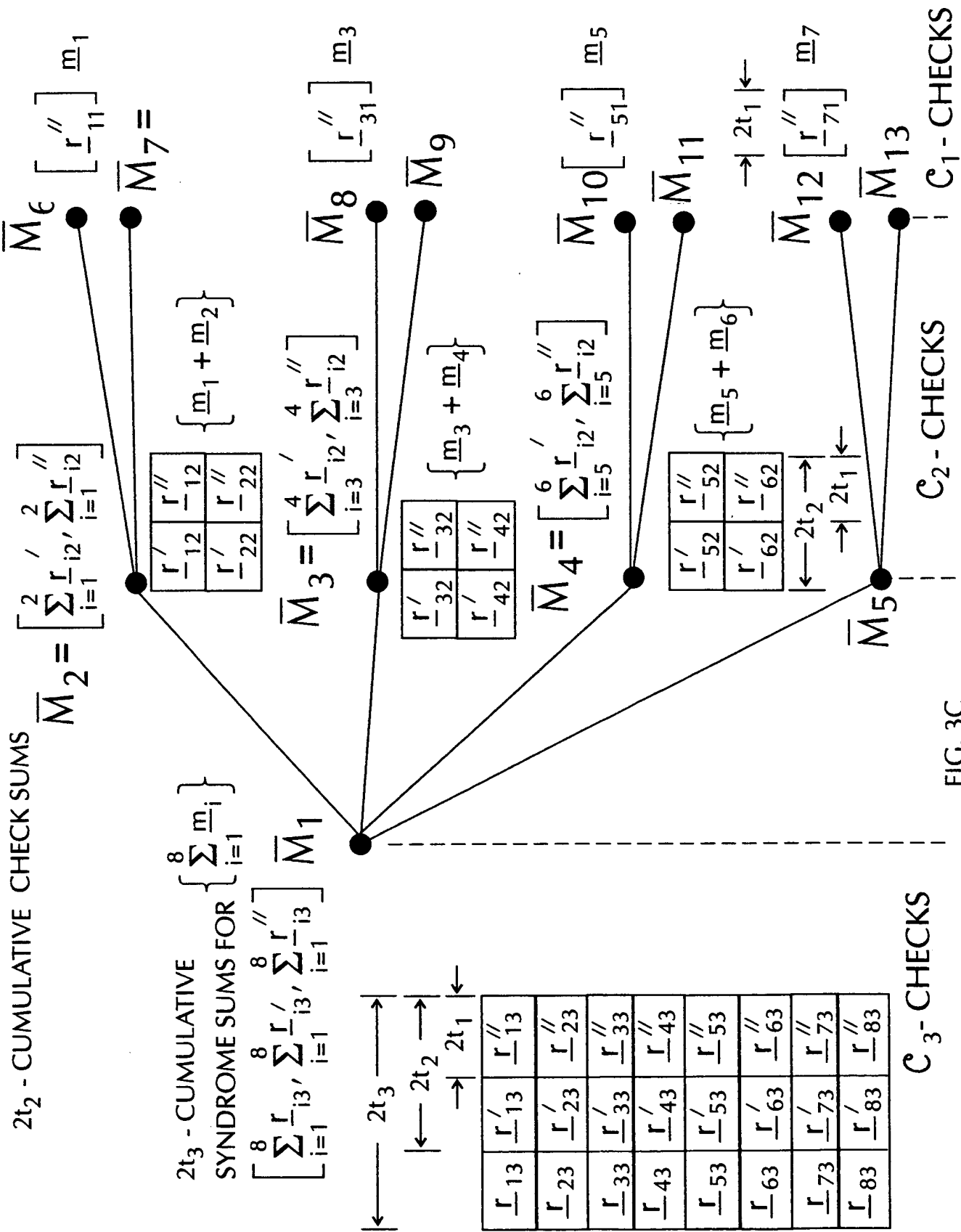
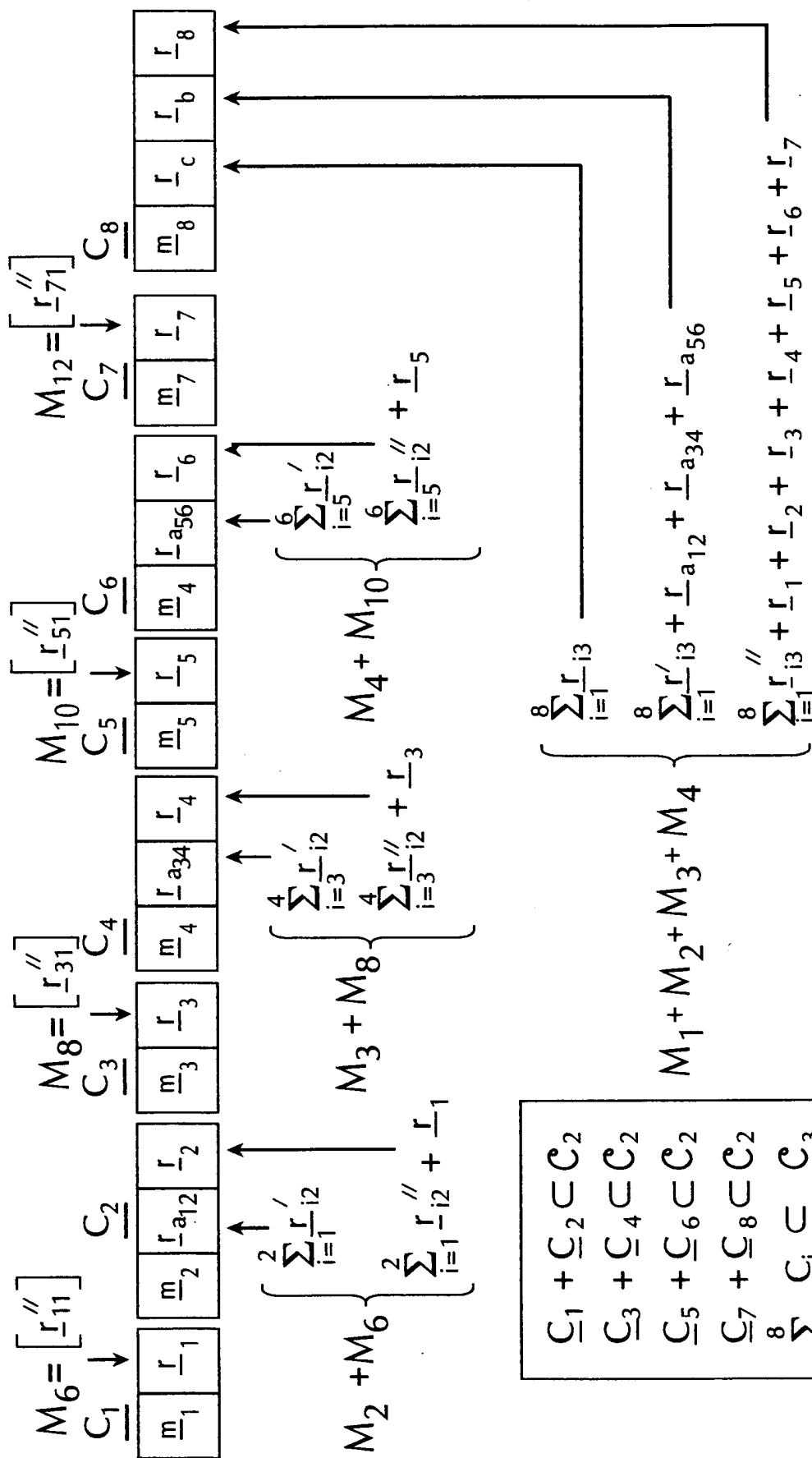


FIG. 3B



MULTI-LEVEL ENCODER CHECK EQUATION



INTEGRATED 8-SECTOR FORMAT

$$C_3 \subset C_2 \subset C_1$$

FIG. 3D

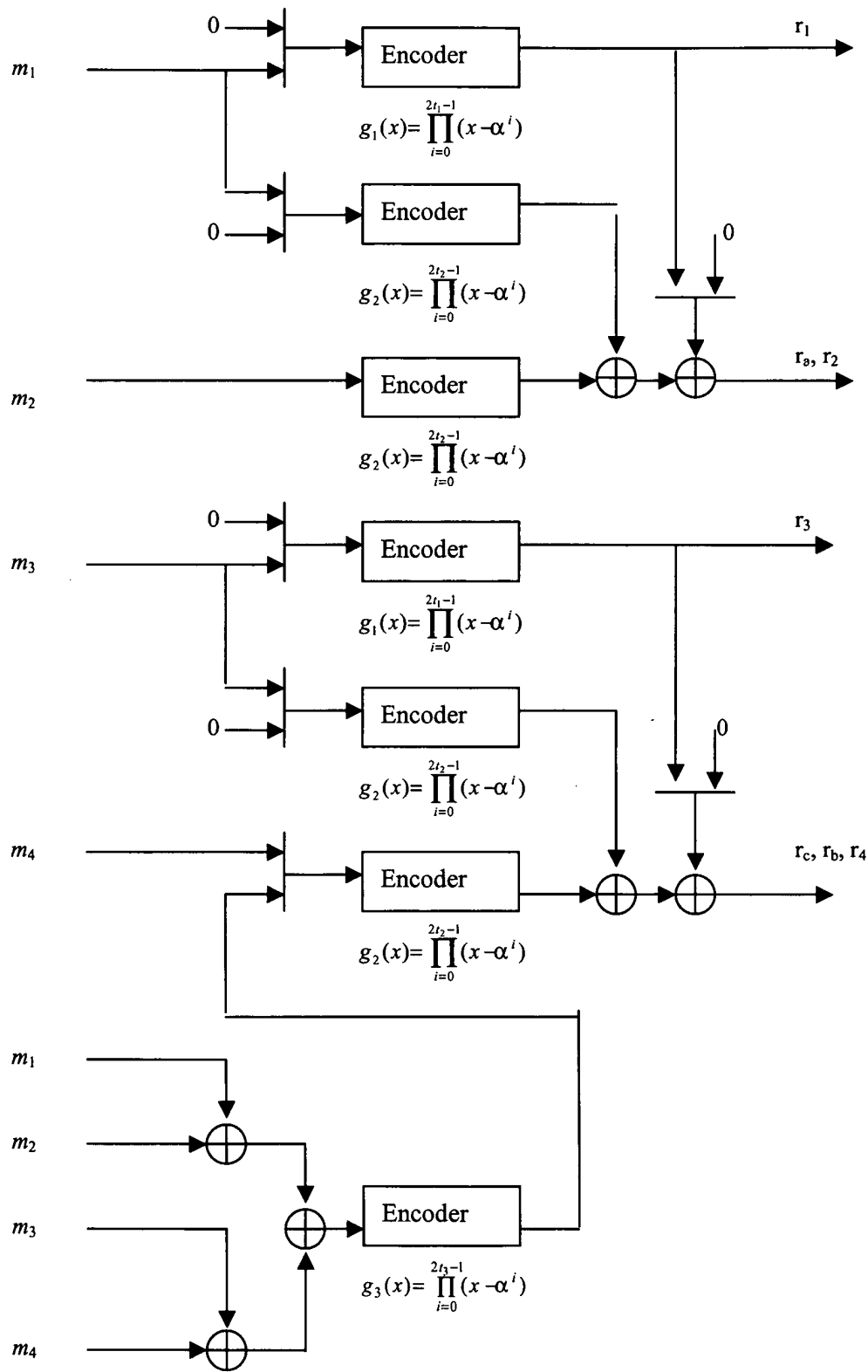


FIG. 4

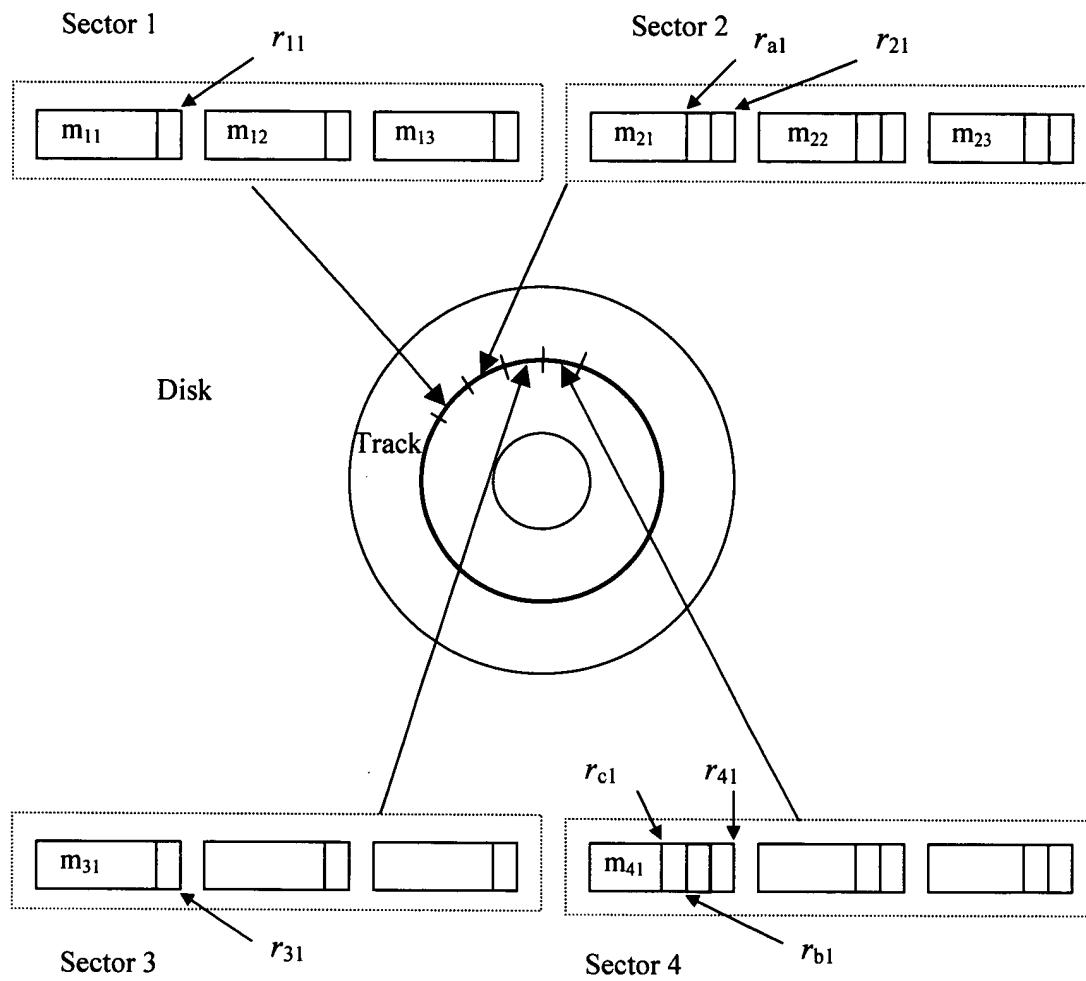


FIG. 5

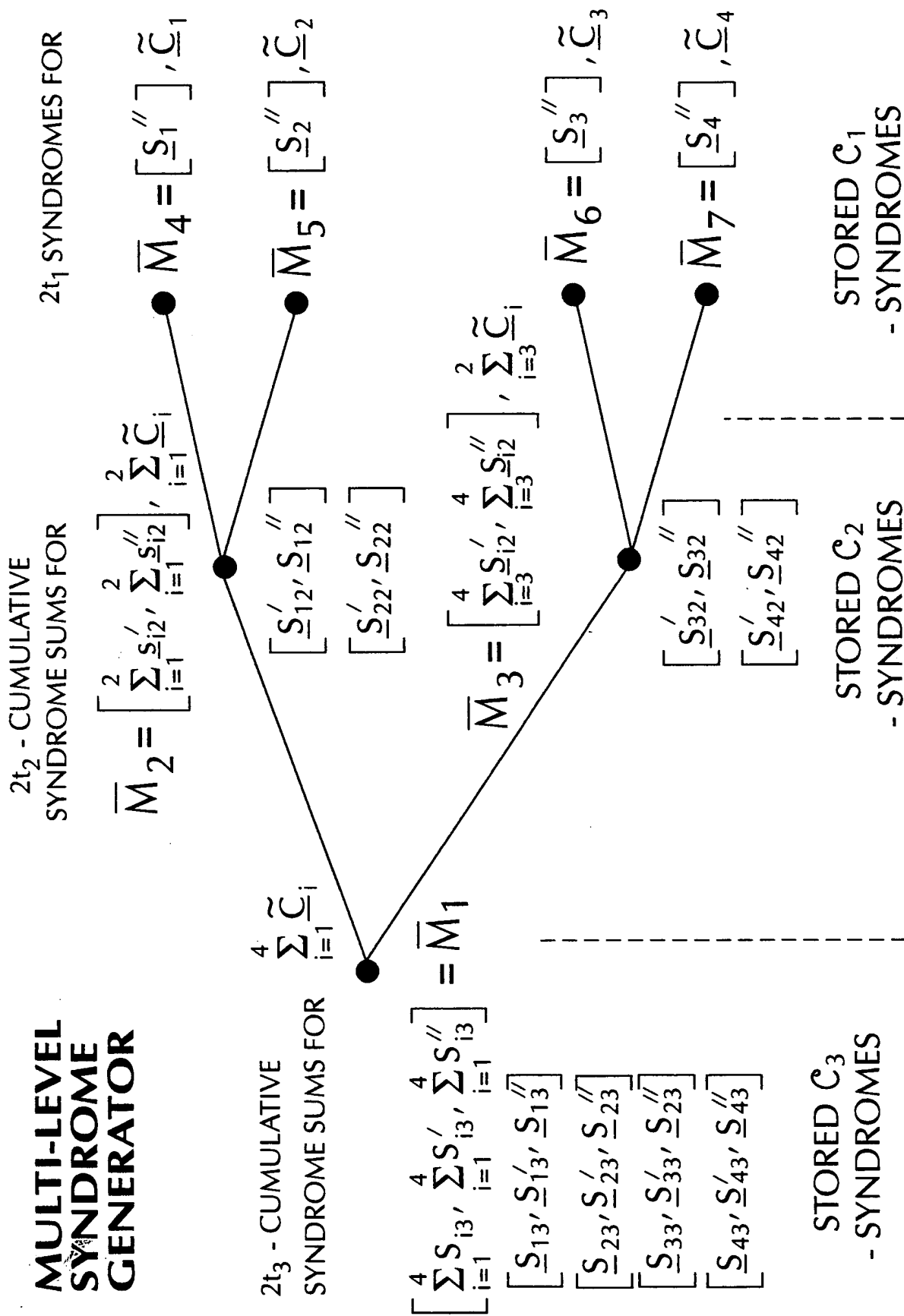


FIG. 6

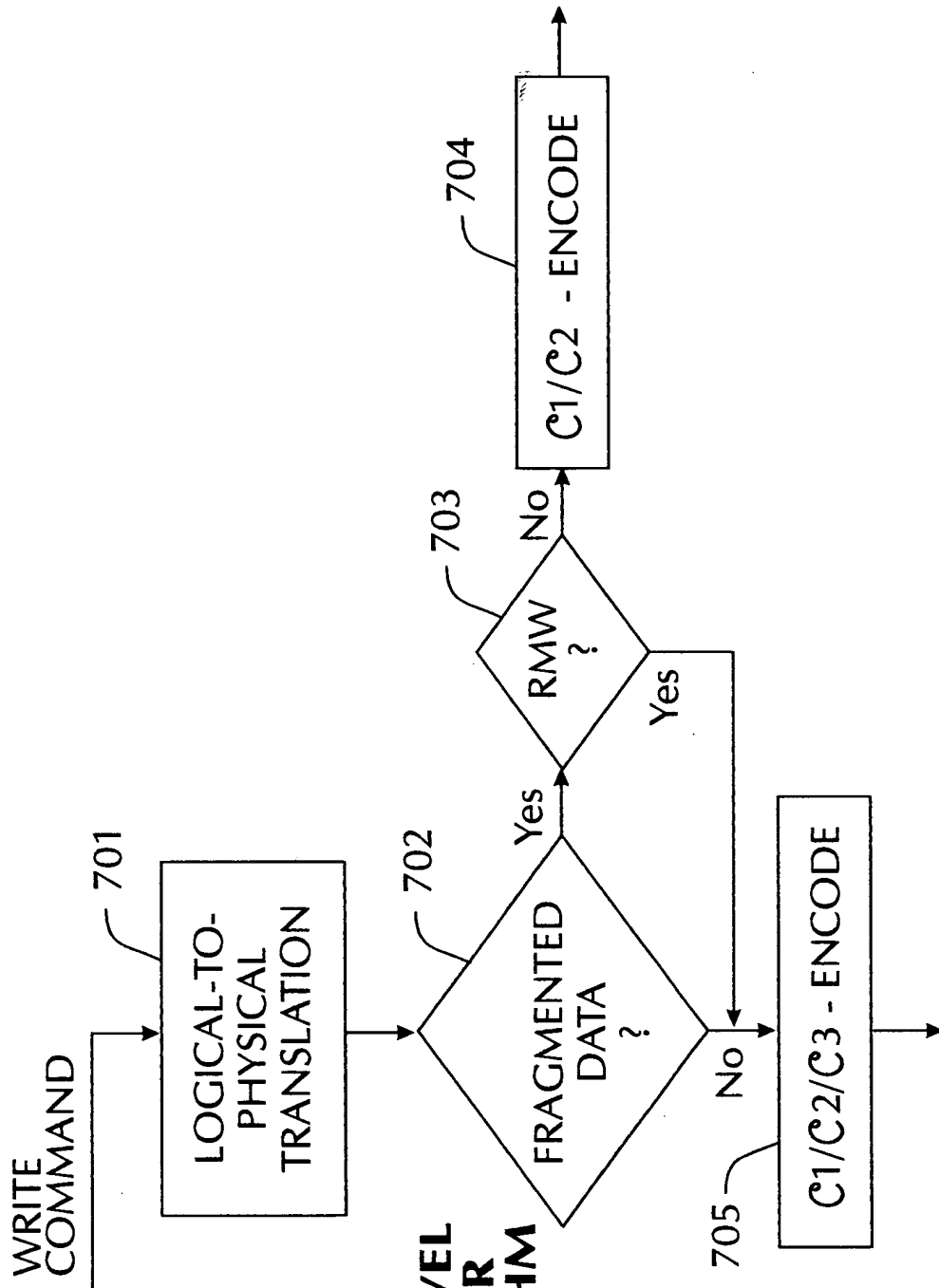


FIG 7
MULTI-LEVEL
ENCODER
ALGORITHM
700

FIG. 8
MULTI-LEVEL
DECODER
ALGORITHM
800

